



Grams: "TECHNOLOGY"
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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
(Established by Andhra Pradesh Act No.30 of 2008)
Kukatpally, Hyderabad – 500 085, Andhra Pradesh (India)

B. TECH ELECTRONICS AND TELEMATICS ENGINEERING

I YEAR

Code	Subject	L	T/P/D	C
	English	2	-	4
	Mathematics - I	3	1	6
	Mathematical Methods	3	-	6
	Engineering Physics	3	-	6
	Engineering Chemistry	3	-	6
	Computer Programming	3	-	6
	Engineering Drawing	2	3	6
	Computer Programming Lab.	-	3	4
	Engineering Physics / Engineering Chemistry Lab.	-	3	4
	English Language Communication Skills Lab.	-	3	4
	IT Workshop / Engineering Workshop	-	3	4
	Total	19	16	56

II YEAR I SEMESTER

Code	Subject	L	T/P/D	C
	Mathematics - III	4	-	4
	Probability Theory and Stochastic Processes	4	-	4
	Switching Theory and Logic Design	4	-	4
	Electrical Circuits	4	-	4
	Electronic Devices and Circuits	4	-	4
	Signals and Systems	4	-	4
	Electronic Devices and Circuits Lab.	-	3	2
	Basic Simulation Lab.	-	3	2
	Total	24	6	28

II YEAR II SEMESTER

Code	Subject	L	T/P/D	C
	Principles of Electrical Engineering	4	-	4
	Electronic Circuit Analysis	4	-	4
	Pulse and Digital Circuits	4	-	4
	Environmental Studies	4	-	4
	Electromagnetic Theory and Transmission Lines	4	-	4
	Analog Communications	4	-	4
	Electrical Technology Lab.	-	3	2
	Electronic Circuits and Pulse Circuits Lab.	-	3	2
	Total	24	6	28

III YEAR I SEMESTER

Code	Subject	L	T/P/D	C
	Control Systems Engineering	4	-	4
	Computer Organization & Operating Systems	4	-	4
	Digital Signal Processing	4	-	4
	Antennas and Wave Propagation	4	-	4
	Digital Communications	4	-	4
	Linear and Digital IC Applications	4	-	4
	Analog and Digital Communications Lab.	-	3	2
	Linear and Digital IC Applications Lab.	-	3	2
	Total	24	6	28

III YEAR II SEMESTER

Code	Subject	L	T/P/D	C
	Managerial Economics and Financial Analysis	4	-	4
	Open Elective: Human Values & Professional Ethics Disaster Management Intellectual Property Rights	4	-	4
	Telecommunication Switching Systems and Networks	4	-	4
	Microprocessors and Microcontrollers	4	-	4
	Computer Networks	4	-	4
	VLSI Design	4	-	4
	Microprocessors and Microcontrollers Lab.	-	3	2
	Advanced Communication Skills Lab.	-	3	2
	Total	24	6	28

IV YEAR I SEMESTER

Code	Subject	L	T/P/D	C
	Management Science	4	-	4
	Multimedia and Signal Coding	4	-	4
	Advanced Telecommunication Technologies	4	-	4
	Electronic Measurements and Instrumentation	4	-	4
	Elective -I: Digital Image Processing Digital Signal Processors and Architectures Spread Spectrum Communications	4	-	4
	Elective -II: Optical Communications Digital Control Systems Embedded Systems Design	4	-	4
	Computer Networks Lab	-	3	2
	Advanced Telecommunications Lab.	-	3	2
	Total	24	6	28

IV YEAR II SEMESTER

Code	Subject	L	T/P/D	C
	Elective -III: Cellular and Mobile Communications Satellite Communications Microwave Engineering	4	-	4
	Elective -IV: Network Security Wireless Communication & Networks Radar Systems	4	-	4
	Elective -V: Pattern Recognition Internetworking Digital Design using Verilog HDL RF Circuit Design	4	-	4
	Industry Oriented Mini Project	-	-	2
	Seminar	-	6	2
	Major Project	-	15	10
	Comprehensive Viva	-	-	2
	Total	12	21	28

Note: All End Examinations (Theory and Practical) are of three hours duration-
T - Tutorial L – Theory P – Practical/Drawing C – Credits